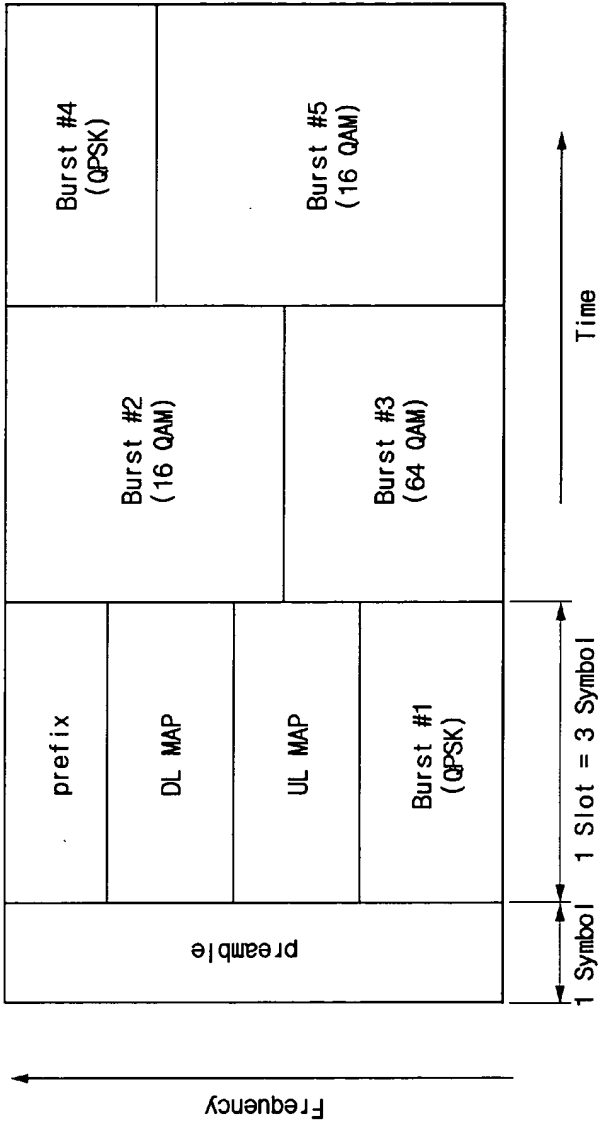
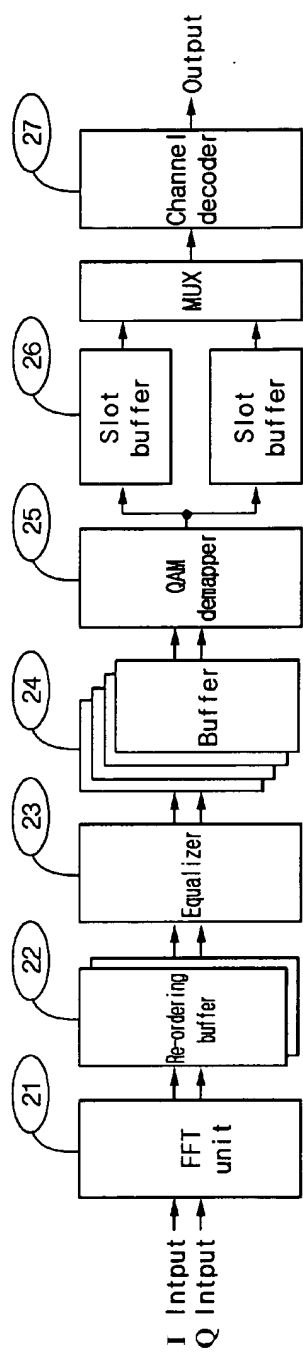


[Fig. 1]

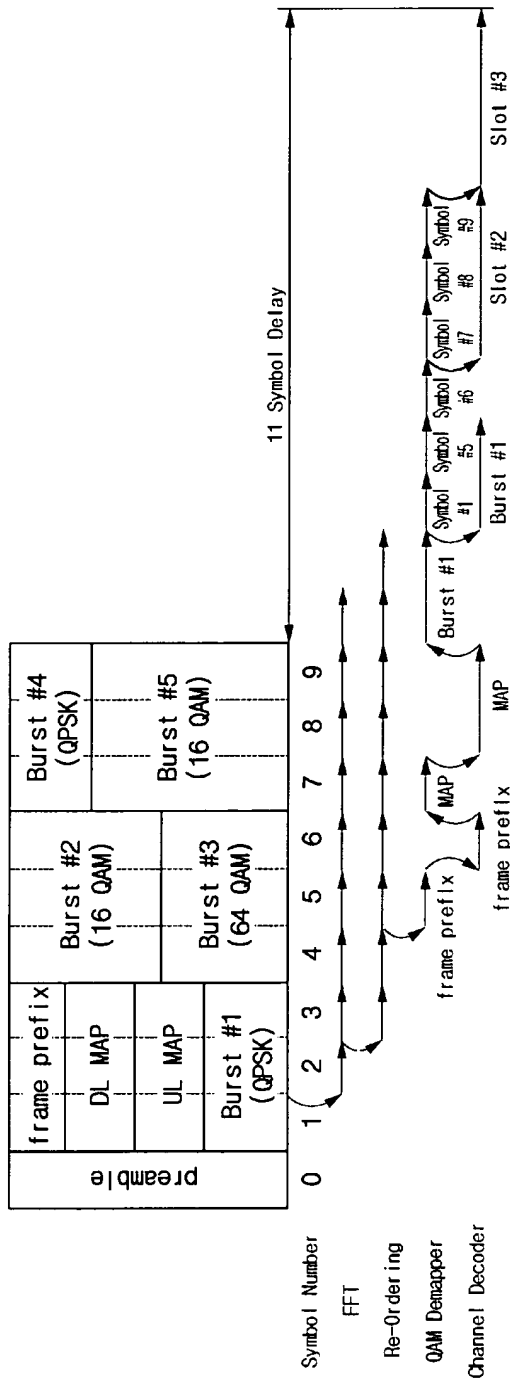


2/7

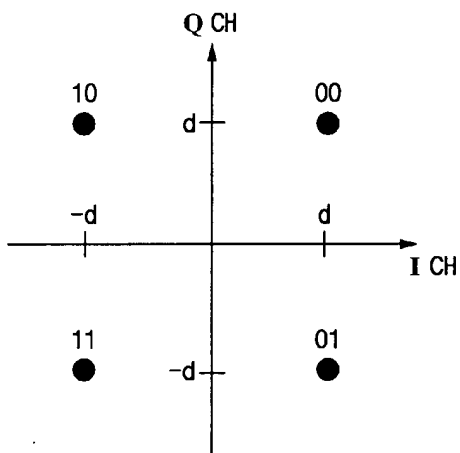
[Fig. 2]



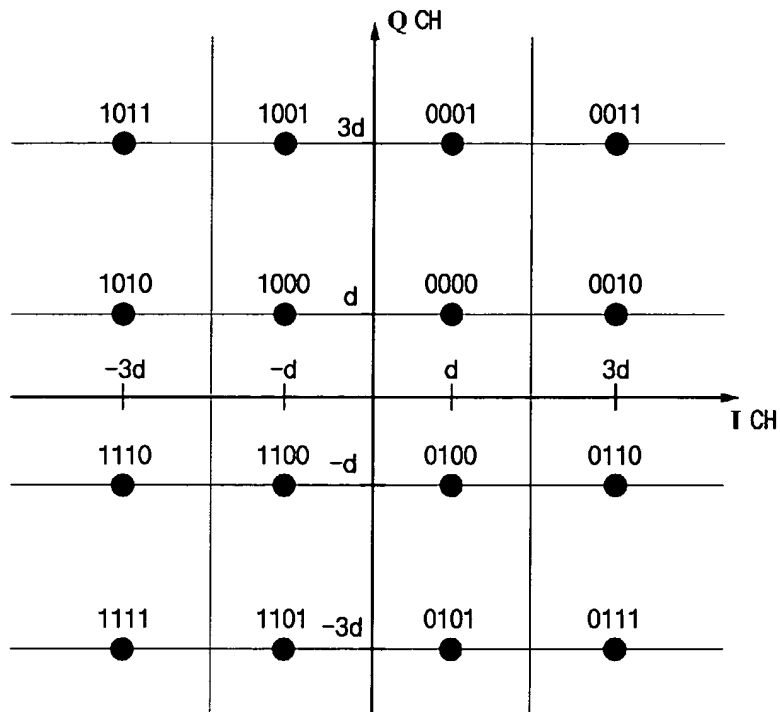
[Fig. 3]



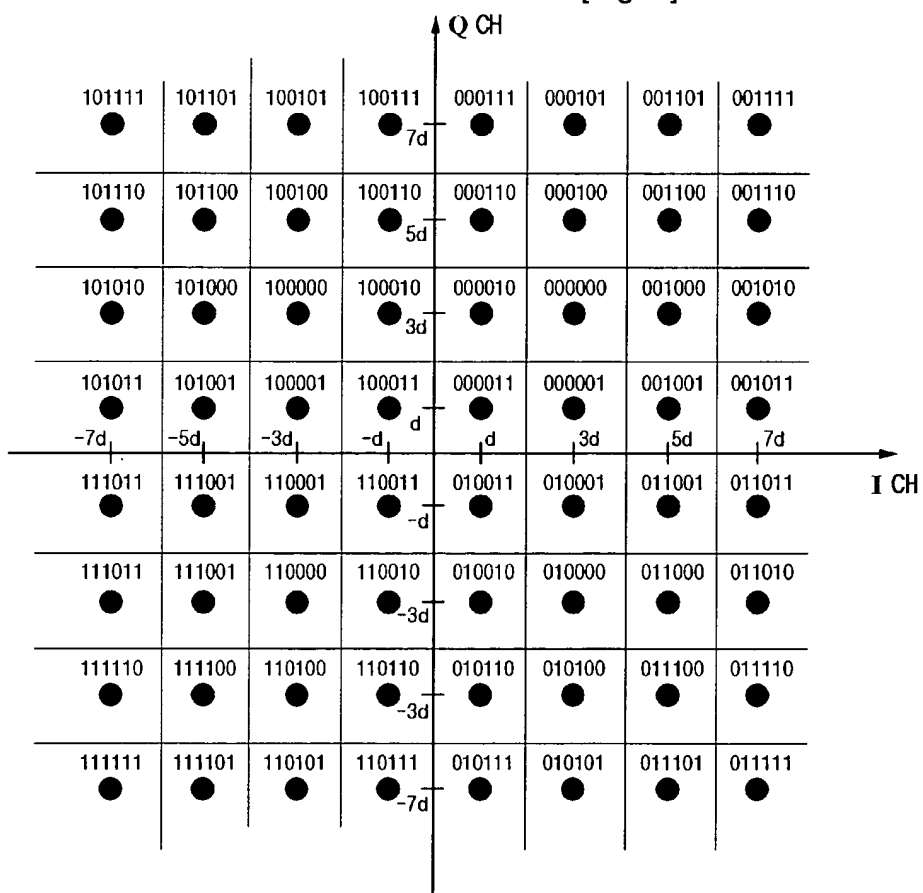
[Fig. 4]



[Fig. 5]



[Fig. 6]

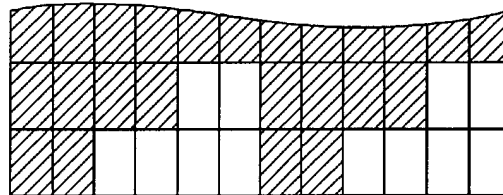
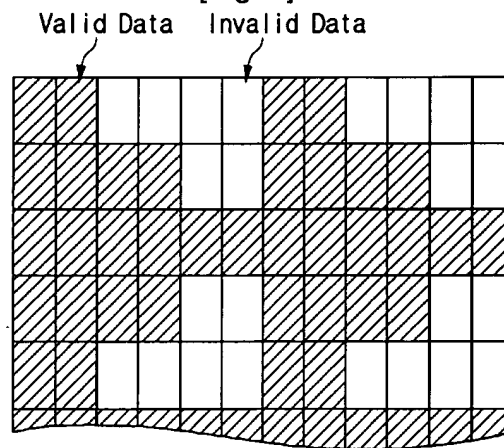


Sub Channel #1 = QPSK  
 Sub Channel #2 = 16QAM  
 Sub Channel #3 = 64QAM  
 Sub Channel #4 = 16QAM  
 Sub Channel #5 = QPSK

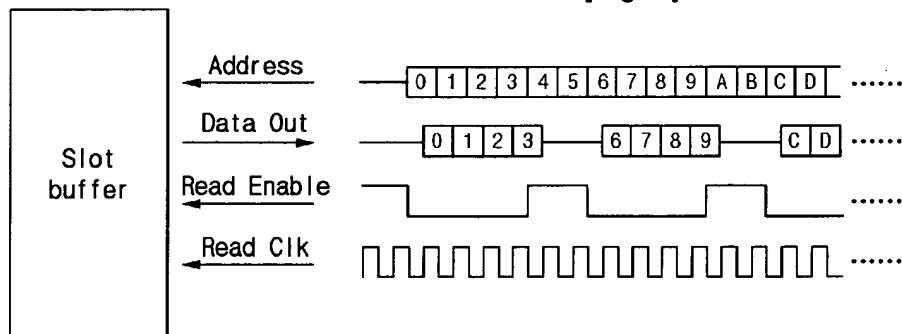
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Sub Channel #31 = 16QAM  
 Sub Channel #32 = QPSK

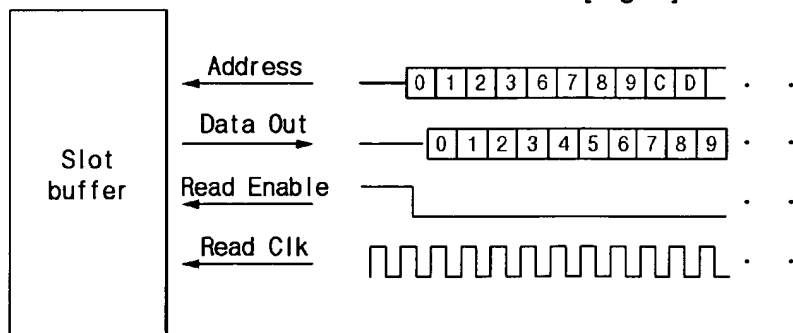
[Fig. 7]



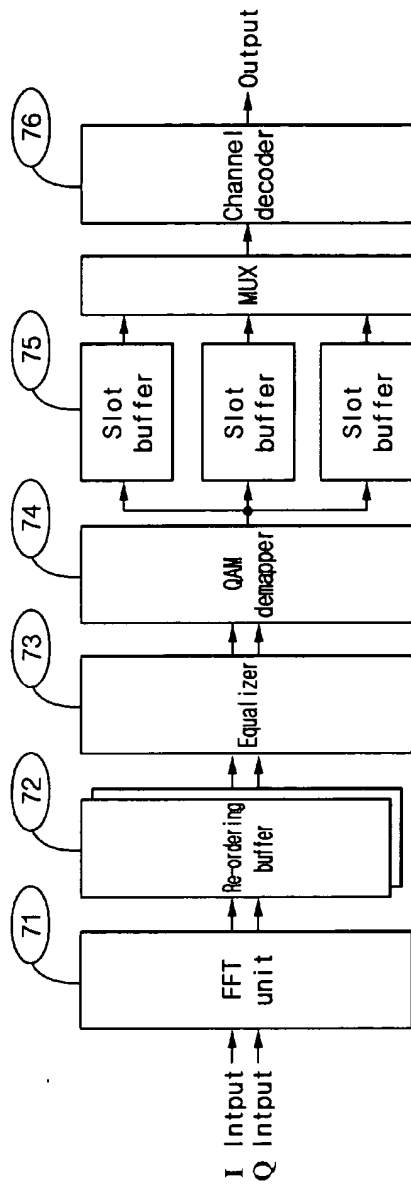
Slot buffer  
 [Fig. 8]



[Fig. 9]



[Fig. 10]



[Fig. 11]

